

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with MANABU KANESAKA, Registration Number 31,467 on October 2, 2009.

Information Disclosure Statement:

2. The information disclosure statement (IDS) submitted on 04/10/2009 is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.
3. Remark: the processor includes only hardware device.

In claims: Please replace the claims filed on 07/10/2009 with the list of below claims:

1 - 5. (Cancelled)

6. (Currently amended) A parameter adjusting device comprising:

a processor;

a plurality of processing devices forming parameter regulation devices configured to optimize parameters using a genetic algorithm;

a first part of said plurality of processing devices being assigned to search using a local search method;

a second part of said plurality of processing devices being assigned to processing of the genetic algorithm, said second part of said plurality of processing devices being respectively configured to individually and simultaneously execute the genetic algorithm by performing predetermined times to determine an individual extraction cycle for the local search method, extracting superior individuals by the predetermined times and transferring the superior individuals to central processing unit for the local search method;

migration devices which, for a predetermined number of generation change processing of the genetic algorithm, send individuals from a parent population of the individuals being processed by the genetic algorithm, to predetermined others of said plurality of processing devices and receive a predetermined number of individuals from predetermined other processing devices to the parent population; and

search processing control configured for collecting interim results of searches from the processing devices assigned to the processing by the genetic algorithm and using search processing by the local search method,

wherein program optimizing parameters using any one of the genetic algorithm and the local search method is installed in one central processing unit (CPU) of the plurality of processing devices or one CPU in a plurality of CPUs of a server, and

wherein each device of said plurality of processing devices includes a determination device determining whether to satisfy search processing termination conditions, and when a termination is determined in a selected optional processing device, an entire processing of the processing devices is terminated.

7 - 10. (Cancelled)

REASONS FOR ALLOWANCE

4. The following is an examiner's statement of reasons for allowance:

Prior arts of record Patent Number US 6,542,468 by Hatakeyama, does not render obvious, nor anticipate the combination of claimed elements including the technique of:

“a second part of said plurality of processing devices being assigned to processing of the genetic algorithm, said second part of said plurality of processing devices being respectively configured to individually and simultaneously execute the genetic algorithm by performing predetermined times

to determine an individual extraction cycle for the local method, extracting superior individuals by the predetermined times and transferring the superior individuals to central processing unit for the local search method; and having migration devices which, for a predetermined-number of generation change processing of the genetic algorithm, send individuals from a parent population of the individuals being processed by the genetic algorithm, to predetermined others of said plurality of processing devices and receive a predetermined number of individuals from predetermined other processing devices to the parent population”

as recited in claim 6. Thus, claim 6 is allowed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

Any inquiry concerning this communication should be directed to CECILE VO whose telephone number is (571)270-3031. The examiner can normally be reached on Mon - Thu (9AM - 5:00PM EST).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tony Mahmoudi can be reached on 571-272-4078. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Cam Y Truong/
Primary Examiner
Art Unit 2169

/Cecile Vo/
Examiner
Art Unit 2169